



PBL REPORT

Operating Systems III-I CSE-B



NOVEMBER 11, 2022
ANURAG UNIVERSITY
school of engineering

<u>S.NO</u>	<u>NAME</u>	<u>ROLLNO</u>	<u>PROBLEM STATEMENTS</u>
Team1	K Likhitha	21EG505225	OS Services
	Akshay Reddy K	21EG505203	
	B Sai Charan	21EG505206	
	Goutham Raj K	21EG505216	
	Devina Nair	21EG505256	
Team2	B.Harshitha	21EG505211	Inter Process Communication
	B.Bhavana	21EG505207	
	Y.Sushma	21EG505252	
	J.Monika	21EG505258	
	V.Sai Nithin Reddy	21EG505253	
Team3	K.Chandhana	21EG505229	OS Structures
	K.S.Lipika	21EG505222	
	Juhi Mohta	21EG505221	
	K.Shailaja	21EG505230	
	K.Hemasri	21EG505240	
Team4	Dheeraj.R	21EG505241	Scheduling algorithms
	Akshay kumar.M	21EG505236	
	Deekshith.P	21EG505239	
	Sai Kiran.B	21EG505210	
	Mahender.B	21EG505254	
Team5	Manognya. G	21EG505257	process synchronization-(Critical section,software solution,hardware solution)
	Sanjay. K	21EG505224	
	Godha devi. A	21EG505202	
	Janasree. G	21EG505218	
	Sathwik goutham	21EG505217	
Team6	B.Rithwikha	21EG505724	classical problems of synchronization
	P.Maheshwari	21EG505725	
	D.Akshitha	21EG505726	
Team7	R Sanjay Kumar	21EG505243	Deadlock Avoidance
	R Suryanarayana	21EG505244	
	P.Harshavardhan Reddy	21EG505238	
	Soumith	21EG505235	
	Saketh Kasyap	21EG505223	

	Nagesh	21EG505209	
Team8	G.Mohan	21EG505213	Deadlock Detection
	G.Pavan	21EG505215	
	K.Rakhesh	21EG505260	
	G.Hari Sharan	21EG505214	
	Shekshavali	21EG505250	
Team9	L. Tejaswee	21EG505231	Paging,Segmentation
	M. Sreshta	21EG505233	
	P. Shivani	21EG505237	
	T. Sumedha	21EG505234	
	S. Nikhila Reddy	21EG505245	
Team10	D. Sireesha	21EG505214	Dining Philosopher
	Anish sai	21EG505205	
	Abhinav Varma	21EG505227	
Team11	S M Akram	21EG505248	virtual memory and demand paging
	Varun Reddy.J	21EG505220	
	Shyam Prakash	21EG505208	
Team12	S Srikanth	21EG505246	Swapping
	G Ajaytrivedi	21EG505219	
	G Akhilesh	21EG505249	
	Manoj kumar	21EG505255	
	Nithin	21EG505259	
Team13	K.Rohith Reddy	21EG505730	Security
	G.Abhinay	21EG505728	
	B.Karthikeya Reddy	21EG505721	
	A.Rahul	21EG505204	
Team14	K.Srikanth	21EG505226	Protection
	shashidhar reddy	21EG505228	
	siddharth reddy	21EG505232	
	saurabh	21EG505212	
	A. Akhil	21EG505201	

REPORT

Our section has formed 14 different teams with 3 to 5 members in each team. All the teams presented innovative solutions for their given problem. The goal for this PBL event is to make students find solutions to given problem, which they will be doing later in their life.

This type of events will be helpful for students by giving them experience of working in a team and also in project. This event was done in presence of Operating Systems faculty Mrs J Himabindu Priyanka.

Presentations: Each team have worked smart and made wonderful presentations on their respective topic



PBL ACTIVITY EVALUATION SHEET

H NO	PROBLEM STATEMENTS	METHODOLOGY/ DESIGN(3 M)	PRESENTATION SKILLS (PPT/ Model / Program Execution) (3 Marks)	Report (4 marks)	Total Marks 10 Marks
21EG505225	Services of OS	3	3	4	10
21EG505203		3	3	3	9
21EG505206		3	3	3	9
21EG505216		3	3	3	9
21EG505256		3	3	4	10
21EG505211	Inter Process Communication	3	3	3	9
21EG505207		3	2	3	8
21EG505252		1	1	3	5
21EG505258		1	1	3	5
21EG505253		3	2	3	8
21EG505229	OS Structures	2	1	2	5
21EG505222		2	3	3	8
21EG505221		2	1	2	5
21EG505230		2	1	2	5
21EG505240		2	1	2	5
21EG505241	Scheduling Algorithms	3	3	4	10
21EG505236		3	2	2	7
21EG505239		3	3	3	9
21EG505210		3	2	2	7
21EG505254		3	3	3	9
21EG505257	process synchronization- (Critical section,software solution,hardware solution)	3	3	2	8
21EG505224		3	3	3	9
21EG505202		3	2	2	7
21EG505218		3	3	2	8
21EG505217		3	2	2	7
21EG505724	classical problems of synchronization	3	3	2	8
21EG505725		3	2	3	8
21EG505726		3	2	3	8
21EG505243	Deadlock Avoidance	3	2	3	8
21EG505244		3	2	3	9
21EG505238		3	3	3	9
21EG505235		3	3	3	9
21EG505223		3	1	2	6
21EG505209		3	1	2	6

21EG505213	Deadlock Detection	3	1	2	6
21EG505215		2	1	2	5
21EG505260		3	3	2	8
21EG505214		3	2	2	7
21EG505250		3	2	2	7
21EG505231	Paging, Segmentation	2	2	2	6
21EG505233		3	2	3	8
21EG505237		3	2	3	8
21EG505234		3	3	2	8
21EG505245		3	3	2	8
21EG505214	Dining Philosopher	2	2	3	7
21EG505205		2	2	2	6
21EG505227		3	3	3	9
21EG505248	Virtual Memory Demand paging	2	1	2	5
21EG505220		2	1	2	5
21EG505208		2	1	2	5
21EG505246	Swapping	3	2	3	8
21EG505219		3	2	3	8
21EG505249		3	2	2	7
21EG505255		3	2	2	7
21EG505259		3	2	3	8
21EG505730	Security	3	3	4	10
21EG505728		3	3	4	10
21EG505721		3	1	4	8
21EG505204		3	2	4	9
21EG505226		Protection	3	2	2
21EG505228	3		2	2	7
21EG505232	3		2	3	8
21EG505212	3		3	4	10
21EG505201	3		2	2	7

Course Instructor

Dean SOE