

**Anurag Group of Institutions**  
**(Autonomous)**  
**Venkatapur (V), Ghatkesar (M), R. R. (Dist.)**  
**I-M.Tech-I-SEMESTER MARKS : ECE**

12H66D7001	BALAKRISHNA MASANAM					
SNO	SUBCODE	SUBNAME	INT	EXT	TOT	RES CRED
1	A31064	Digital System Design	29	38	67	P 3
2	A31063	Advanced Digital Signal Processing	37	46	83	P 3
3	A31059	VLSI Technolgy & Design	35	26	61	P 3
4	A31074	Advanced Data Communications	34	47	81	P 3
5	A31057	Microcontrollers for Embedded System Design	31	41	72	P 3
6	A31058	Embedded Real Time Operating Systems	35	55	90	P 3
7	A31219	Signal Processing Lab	35	51	86	P 2
8	A31220	Seminar	42	0	42	P 2
		TOTAL	278	304	582	PASS 22
12H66D7002	DEVARAJU JYOTHI					
SNO	SUBCODE	SUBNAME	INT	EXT	TOT	RES CRED
1	A31064	Digital System Design	23	9	32	F 0
2	A31063	Advanced Digital Signal Processing	35	41	76	P 3
3	A31059	VLSI Technolgy & Design	33	24	57	P 3
4	A31074	Advanced Data Communications	34	35	69	P 3
5	A31057	Microcontrollers for Embedded System Design	25	31	56	P 3
6	A31058	Embedded Real Time Operating Systems	33	50	83	P 3
7	A31219	Signal Processing Lab	33	45	78	P 2
8	A31220	Seminar	47	0	47	P 2
		TOTAL	263	235	498	PROMOT 19
12H66D7003	DHARAVATHU MURALIKRISHNA					
SNO	SUBCODE	SUBNAME	INT	EXT	TOT	RES CRED
1	A31064	Digital System Design	19	16	35	F 0
2	A31063	Advanced Digital Signal Processing	32	47	79	P 3
3	A31059	VLSI Technolgy & Design	33	24	57	P 3
4	A31074	Advanced Data Communications	32	26	58	P 3
5	A31057	Microcontrollers for Embedded System Design	26	30	56	P 3
6	A31058	Embedded Real Time Operating Systems	34	39	73	P 3
7	A31219	Signal Processing Lab	36	52	88	P 2
8	A31220	Seminar	42	0	42	P 2
		TOTAL	254	234	488	PROMOT 19
12H66D7004	G ASHOK KUMAR					
SNO	SUBCODE	SUBNAME	INT	EXT	TOT	RES CRED
1	A31064	Digital System Design	16	13	29	F 0
2	A31063	Advanced Digital Signal Processing	20	30	50	P 3
3	A31059	VLSI Technolgy & Design	21	20	41	F 0
4	A31074	Advanced Data Communications	28	24	52	P 3
5	A31057	Microcontrollers for Embedded System Design	18	32	50	P 3
6	A31058	Embedded Real Time Operating Systems	16	27	43	F 0
7	A31219	Signal Processing Lab	25	47	72	P 2
8	A31220	Seminar	0	0	0	F 0
		TOTAL	144	193	337	PROMOT 11
12H66D7005	K SHREE CHANDANA MAYURI					
SNO	SUBCODE	SUBNAME	INT	EXT	TOT	RES CRED
1	A31064	Digital System Design	25	42	67	P 3
2	A31063	Advanced Digital Signal Processing	35	51	86	P 3
3	A31059	VLSI Technolgy & Design	39	45	84	P 3

4	A31074	Advanced Data Communications	36	40	76	P	3
5	A31057	Microcontrollers for Embedded System Design	33	44	77	P	3
6	A31058	Embedded Real Time Operating Systems	35	46	81	P	3
7	A31219	Signal Processing Lab	38	56	94	P	2
8	A31220	Seminar	46	0	46	P	2
		TOTAL	287	324	611	PASS	22
12H66D7006 KALANGI MARY CHRISTINA							
SNO	SUBCODE	SUBNAME	INT	EXT	TOT	RES	CRED
1	A31064	Digital System Design	23	37	60	P	3
2	A31063	Advanced Digital Signal Processing	28	51	79	P	3
3	A31059	VLSI Technolgy & Design	32	39	71	P	3
4	A31074	Advanced Data Communications	31	35	66	P	3
5	A31057	Microcontrollers for Embedded System Design	29	49	78	P	3
6	A31058	Embedded Real Time Operating Systems	30	51	81	P	3
7	A31219	Signal Processing Lab	36	54	90	P	2
8	A31220	Seminar	46	0	46	P	2
		TOTAL	255	316	571	PASS	22
12H66D7007 MD ARIF AHMED							
SNO	SUBCODE	SUBNAME	INT	EXT	TOT	RES	CRED
1	A31064	Digital System Design	18	32	50	P	3
2	A31063	Advanced Digital Signal Processing	32	40	72	P	3
3	A31059	VLSI Technolgy & Design	34	25	59	P	3
4	A31074	Advanced Data Communications	31	37	68	P	3
5	A31057	Microcontrollers for Embedded System Design	27	39	66	P	3
6	A31058	Embedded Real Time Operating Systems	26	38	64	P	3
7	A31219	Signal Processing Lab	34	50	84	P	2
8	A31220	Seminar	48	0	48	P	2
		TOTAL	250	261	511	PASS	22
12H66D7008 MOULI KADALI							
SNO	SUBCODE	SUBNAME	INT	EXT	TOT	RES	CRED
1	A31064	Digital System Design	22	28	50	P	3
2	A31063	Advanced Digital Signal Processing	29	33	62	P	3
3	A31059	VLSI Technolgy & Design	35	15	50	F	0
4	A31074	Advanced Data Communications	31	41	72	P	3
5	A31057	Microcontrollers for Embedded System Design	24	42	66	P	3
6	A31058	Embedded Real Time Operating Systems	33	52	85	P	3
7	A31219	Signal Processing Lab	31	47	78	P	2
8	A31220	Seminar	42	0	42	P	2
		TOTAL	247	258	505	PROMOT	19

12H66D7009		NAGULA MARUTHI					
SNO	SUBCODE	SUBNAME	INT	EXT	TOT	RES	CRED
1	A31064	Digital System Design	17	22	39	F	0
2	A31063	Advanced Digital Signal Processing	26	33	59	P	3
3	A31059	VLSI Technolgy & Design	26	24	50	P	3
4	A31074	Advanced Data Communications	24	19	43	F	0
5	A31057	Microcontrollers for Embedded System Design	24	34	58	P	3
6	A31058	Embedded Real Time Operating Systems	29	38	67	P	3
7	A31219	Signal Processing Lab	30	50	80	P	2
8	A31220	Seminar	42	0	42	P	2
TOTAL			218	220	438	PROMOT	16
12H66D7010		NOMULA SARITHA RANI					
SNO	SUBCODE	SUBNAME	INT	EXT	TOT	RES	CRED
1	A31064	Digital System Design	21	23	44	F	0
2	A31063	Advanced Digital Signal Processing	31	40	71	P	3
3	A31059	VLSI Technolgy & Design	38	31	69	P	3
4	A31074	Advanced Data Communications	31	32	63	P	3
5	A31057	Microcontrollers for Embedded System Design	27	26	53	P	3
6	A31058	Embedded Real Time Operating Systems	35	48	83	P	3
7	A31219	Signal Processing Lab	34	52	86	P	2
8	A31220	Seminar	47	0	47	P	2
TOTAL			264	252	516	PROMOT	19
12H66D7011		RUDRA SANTHOSHI					
SNO	SUBCODE	SUBNAME	INT	EXT	TOT	RES	CRED
1	A31064	Digital System Design	20	12	32	F	0
2	A31063	Advanced Digital Signal Processing	36	42	78	P	3
3	A31059	VLSI Technolgy & Design	30	16	46	F	0
4	A31074	Advanced Data Communications	30	17	47	F	0
5	A31057	Microcontrollers for Embedded System Design	27	37	64	P	3
6	A31058	Embedded Real Time Operating Systems	36	48	84	P	3
7	A31219	Signal Processing Lab	32	50	82	P	2
8	A31220	Seminar	44	0	44	P	2
TOTAL			255	222	477	PROMOT	13
12H66D7012		VANGALA SPANDANA REDDY					
SNO	SUBCODE	SUBNAME	INT	EXT	TOT	RES	CRED
1	A31064	Digital System Design	26	44	70	P	3
2	A31063	Advanced Digital Signal Processing	34	55	89	P	3
3	A31059	VLSI Technolgy & Design	39	41	80	P	3
4	A31074	Advanced Data Communications	32	47	79	P	3
5	A31057	Microcontrollers for Embedded System Design	34	49	83	P	3
6	A31058	Embedded Real Time Operating Systems	36	54	90	P	3
7	A31219	Signal Processing Lab	37	56	93	P	2
8	A31220	Seminar	47	0	47	P	2
TOTAL			285	346	631	PASS	22
12H66D7013		VELDANDI MAHESH					
SNO	SUBCODE	SUBNAME	INT	EXT	TOT	RES	CRED
1	A31064	Digital System Design	21	29	50	P	3
2	A31063	Advanced Digital Signal Processing	27	50	77	P	3
3	A31059	VLSI Technolgy & Design	31	26	57	P	3
4	A31074	Advanced Data Communications	32	24	56	P	3
5	A31057	Microcontrollers for Embedded System Design	22	31	53	P	3
6	A31058	Embedded Real Time Operating Systems	24	27	51	P	3
7	A31219	Signal Processing Lab	32	49	81	P	2
8	A31220	Seminar	42	0	42	P	2
TOTAL			231	236	467	PASS	22