

Anurag Group of Institutions
(Autonomous)

I M.TECH -II-SEMESTER SUPPLEMENTARY Examinations Result APRIL / MAY- 2014-15

Memorandum of Marks

13H61D5509			VANAMA SOWJANYA					
S.NO	CODE	SUBJECT NAME	INT	EXT	TOT	RES	CRE	
1	A32060	Low Power VLSI Design	30	28	58	P	3	
		TOTAL	30	28	58		3	

13H61D5513			G.VISHWNATH					
S.NO	CODE	SUBJECT NAME	INT	EXT	TOT	RES	CRE	
1	A32057	Architectures	30	20	50	F	0	
		TOTAL	30	20	50		0	