



ANURAG GROUP OF INSTITUTIONS
(Autonomous)

Venkatapur (V), Ghatkesar (M), R.R.Dist - 500088.

Regulation: R12

EXAMINATION BRANCH
SUPPLEMENTARY EXAMINATIONS FEBRUARY / MARCH-2016.

I-M.TECH- I-SEMESTER

TIME TABLE

Date: 17.02.2016.

M.Tech / Branch	29.02.2016 (Monday)	02.03.2016 (Wednesday)	04.03.2016 (Friday)	08.03.2016 (Tuesday)	10.03.2016 (Thursday)	14.03.2016 (Monday)
	10:00 AM to -1:00 PM	10:00 AM to -1:00 PM	10:00 AM to -1:00 PM	10:00 AM to -1:00 PM	10:00 AM to -1:00 PM	10:00 AM to -1:00 PM
Structural Engineering	Computer Oriented Numerical Methods	Theory of Elasticity and plasticity	Theory and Analysis of plates	Advanced Reinforced Concrete Design	Concrete Technology	Advanced Structural Analysis
Computer Science Engg.	Advanced Problem Solving	Computer System Design	Object Oriented Modeling	Java and Web Technologies	Software Design and Engineering	Image Processing and Pattern Recognition
Computer Science	Advanced Problem Solving	Computer System Design	Object Oriented Modeling	Java and Web Technologies	Software Design and Engineering	Image Processing and Pattern Recognition
Computer Networks & Information Security	Advanced Data Structures and Algorithms	Web Technologies and Services	Principles of Information Security	Advanced Computer Networks	Distributed Systems	Network Programming
Software Engg.	Software Requirements & Estimation	Software Process & Project Management	Object Oriented Modeling	Java and Web Technologies	Wireless Networks & Mobile Computing	Information Retrieval Systems
VLSI	Microcontrollers for Embedded System Design	CPLD & FPGA Architectures and Applications	VLSI Technology & Design	Algorithms for VLSI Design Automation	Digital System Design	Advanced Digital Signal Processing
Embedded Systems	Microcontrollers for Embedded System Design	Embedded Real Time Operating Systems	VLSI Technology & Design	Advanced Computer Networks	Digital System Design	Advanced Digital Signal processing
ECE	Microcontrollers for Embedded System Design	Embedded Real Time Operating Systems	VLSI Technology & Design	Advanced Data Communications	Digital System Design	Advanced Digital Signal processing
Wireless and Mobile Communication	Image and Video Processing	TCP/IP and ATM Networks	Satellite Communications	Advanced Data Communications	RF Circuit Design	Advanced Digital Signal Processing
PE&ED	Machine Modelling & Analysis	Renewable Energy Systems	Modern Control Theory	Power Electronic Control of DC Drivers	Programmable Logic Controllers and their Applications	Power Electronic Convertors
EPS	Advanced Power System Analysis	Advanced Power System Protection	Modern Control Theory	Flexible AC Transmission Systems	Energy Auditing, Conservation and Management	Power Electronic Convertors
CAD / CAM	Advanced CAD	Finite Element Analysis	Precision Engineering	Design for Manufacturing and Assembly	Mechatronics	Total Quality Management
Machine Design	Advanced CAD	Advanced Finite Element Analysis	Fatigue & Fracture Mechanics	Computational Methods in Engineering	Advanced Mechanical Engineering Design	Advanced Mechanics of Solids

Controller of Examinations

Principal



ANURAG GROUP OF INSTITUTIONS

Regulation: R12

(Autonomous)

Venkatapur (V), Ghatkesar (M), R.R.Dist - 500088.

EXAMINATION BRANCH

SUPPLEMENTARY EXAMINATIONS, FEBRUARY / MARCH-2016

I-M.TECH- II-SEMESTER

TIME TABLE

Date: 17.02.2016

M.Tech / Branch	01.03.2016 (Tuesday)	03.03.2016 (Thursday)	05.03.2016 (Saturday)	09.03.2016 (Wednesday)	11.03.2016 (Friday)	15.03.2016 (Tuesday)
	10:00 AM to -1:00 PM	10:00 AM to -1:00 PM	10:00 AM to -1:00 PM	10:00 AM to -1:00 PM	10:00 AM to -1:00 PM	10:00 AM to -1:00 PM
Structural Engineering	Finite Element Methods	Structural Dynamics	Analysis and Design of Shells and Folded plates	Advanced. Steel Design	Pre Stressed Concrete	Earthquake Resistant Design of Buildings
Computer Science Engg.	Distributed Computing	Advanced databases	Advanced computer architecture	Advanced computer networks	Information retrieval systems	Information Security
Computer Science	Distributed Computing	Advanced databases	Advanced compiler Design	Advanced computer networks	Information retrieval systems	Information Security
Computer Networks & Information Security	Wireless Networks and Mobile Computing	Applications of Network Security	Information Security Management and Standards	Ad hoc and Sensor Networks	Wireless Security	Cloud Computing
Software Engg.	Distributed Computing	Software Quality Assurance & Testing	Software Architecture & Design patterns	Service Oriented Architecture	Scripting Languages	Introduction to Neural Networks
VLSI	System On Chip Architecture	CMOS Analog & Mixed Signal Design	Embedded Real Time Operating Systems	Design Of Fault Tolerant Systems	Digital Signal Processors and Architecture	Low Power VLSI Design
Embedded Systems	System on Chip Architecture	Hardware Software Co-Design	System Modeling And Simulation	CPLD & FPGA Architectures And Applications	Digital Signal Processors and Architecture	Low Power VLSI Design
ECE	Image Video Processing	Coding Theory And Techniques	Optical Communications Technology	Wireless Communications & Networks	Digital Signal Processors and Architecture	Radar Signal Processing
Wireless and Mobile Communication	Wireless Communications & Networks	Coding Theory and Techniques	Optical Communications Technology	Telecommunication Switching Systems and Networks	Mobile Computing Technologies	Network Security And Cryptography
PE&ED	Power Electronic Control of AC Drives	Modern Power Electronics	Dynamics of Electrical Machines	Flexible AC Transmission Systems	Power Quality	HVDC Transmission
EPS	Power system Control and Stability	High Voltage DC Transmission	Extra High voltage transmission	Distribution Automation	Power Quality	Reactive Power Compensation and management
CAD / CAM	Design of Hydraulics and Pneumatic Systems	Flexible manufacturing systems	Computer Aided Manufacturing	Industrial Robotics	Automation in Manufacturing	Computer Aided Process Planning
Machine Design	Advanced Mechanics of Machinery	Mechanical Vibrations	Advanced Optimization Techniques and Applications	Industrial Robotics	Experimental Stress Analysis	Mechatronics

Controller of Examinations

Principal